Modeling Concurrent Systems

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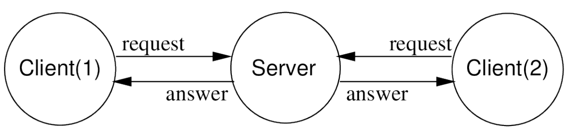
A Client/Server System



- System of one server and two clients.
  - Three concurrently executing system components.
- Server manages a resource.
  - An object that only one system component may use at any time.
- Clients request resource and, having received an answer, use it.
  - Server ensures that not both clients use resource simultaneously.
  - Server eventually answers every request.

Set of system requirements.

System Implementation

```
Server:
  local given, waiting, sender
begin
  given := 0; waiting := 0
  loop
    sender := receiveRequest()
    if sender = given then
      if waiting = 0 then
        given := 0
      else
        given := waiting; waiting := 0
        sendAnswer(given)
      endif
    elsif given = 0 then
      given := sender
      sendAnswer(given)
    else
      waiting := sender
    endif
  endloop
end Server
```

```
Client(ident):
  param ident
begin
  loop
    ...
    sendRequest()
    receiveAnswer()
    ... // critical region
    sendRequest()
  endloop
end Client
```

Desired System Properties

- Property: mutual exclusion.
  - At no time, both clients are in critical region.
    - Critical region: program region after receiving resource from server and before returning resource to server.
  - The system shall only reach states, in which mutual exclusion holds.
- Property: no starvation.
  - Always when a client requests the resource, it eventually receives it.
  - Always when the system reaches a state, in which a client has requested a resource, it shall later reach a state, in which the client receives the resource.
- Problem: each system component executes its own program.
  - Multiple program states exist at each moment in time.
  - Total system state is combination of individual program states.
  - Not easy to see which system states are possible.

How can we verify that the system has the desired properties?

System States

At each moment in time, a system is in a particular state.

- A state $s : Var \rightarrow Val$
  - A state s is a mapping of every system variable x to its value $s(x)$.
    - Typical notation: $s = [x = 0, y = 1, \ldots] = [0, 1, \ldots]$.
  - Var ... the set of system variables
    - Program variables, program counters, ...
  - Val ... the set of variable values.
- The state space $State = \{s \mid s : Var \rightarrow Val\}$
  - The state space is the set of possible states.
    - The system variables can be viewed as the coordinates of this space.
  - The state space may (or may not) be finite.
    - If $|Var| = n$ and $|Val| = m$, then $|State| = m^n$.
    - A word of $\log_2 m^n$ bits can represent every state.

A system execution can be described by a path $s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \ldots$ in the state space.

Deterministic Systems

In a sequential system, each state typically determines its successor state.

- The system is deterministic.
  - We have a (possibly not total) transition function F on states.
  - $s_1 = F(s_0)$ means "s_1 is the successor of s_0".
- Given an initial state s_0, the execution is thus determined.
  - $s_0 \rightarrow s_1 = F(s_0) \rightarrow s_2 = F(s_1) \rightarrow \ldots$
- A deterministic system (model) is a pair $\langle I, F\rangle$.
  - A set of initial states $I \subseteq State$
    - Initial state condition $I(s) :\Leftrightarrow s \in I$
  - A transition function $F : State \overset{partial}{\rightarrow} State$.
- A run of a deterministic system $\langle I, F\rangle$ is a (finite or infinite) sequence $s_0 \rightarrow s_1 \rightarrow \ldots$ of states such that
  - $s_0 \in I$ (respectively $I(s_0)$).
  - $s_{i+1} = F(s_i)$ (for all sequence indices i)
  - If s ends in a state s_n, then F is not defined on s_n.

Nondeterministic Systems

In a concurrent system, each component may change its local state, thus the successor state is not uniquely determined.

- The system is nondeterministic.
  - We have a transition relation R on states.
  - $R(s_0, s_1)$ means "s_1 is a (possible) successor of s_0".
- Given an initial state s_0, the execution is not uniquely determined.
  - Both $s_0 \to s_1 \to \ldots$ and $s_0 \to s_1' \to \ldots$ are possible.
- A non-deterministic system (model) is a pair $\langle I, R \rangle$.
  - A set of initial states (initial state condition) $I \subseteq State$.
  - A transition relation $R \subseteq State \times State$.
- A run s of a nondeterministic system $\langle I, R \rangle$ is a (finite or infinite) sequence $s_0 \to s_1 \to s_2 \ldots$ of states such that
  - $s_0 \in I$ (respectively $I(s_0)$).
  - $R(s_i, s_{i+1})$ (for all sequence indices i).
  - If s ends in a state s_n, then there is no state t such that $R(s_n, t)$.

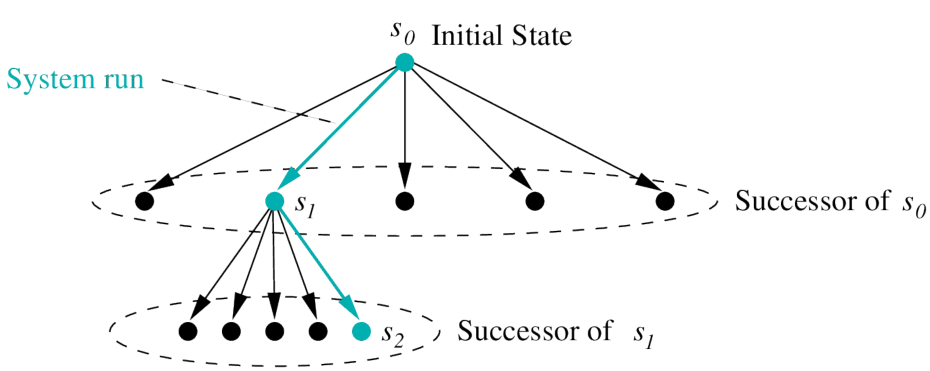
Derived Notions

- Successor and predecessor:
  - State t is a (direct) successor of state s, if $R(s, t)$.
  - State s is then a predecessor of t.
    - A finite run $s_0 \to \ldots \to s_n$ ends in a state which has no successor.
- Reachability:
  - A state t is reachable, if there exists some run $s_0 \to s_1 \to s_2 \to \ldots$ such that $t = s_i$ (for some i).
  - A state t is unreachable, if it is not reachable.

Not all states are reachable (typically most are unreachable).

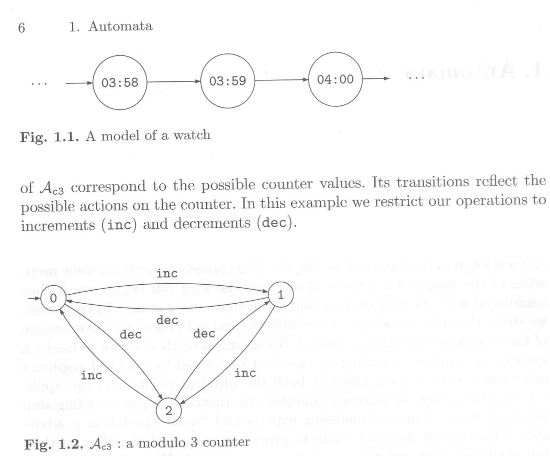
Reachability Graph

The transitions of a system can be visualized by a graph.



The nodes of the graph are the reachable states of the system.

Examples



6 1. Automata

Fig. 1.1. A model of a watch

of $\mathcal{A}_{c3}$ correspond to the possible counter values. Its transitions reflect the possible actions on the counter. In this example we restrict our operations to increments (inc) and decrements (dec).

Fig. 1.2. $\mathcal{A}_{c3}$: a modulo 3 counter

B.Berard et al: "Systems and Software Verification", 2001.

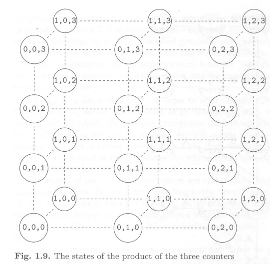
Examples

- A deterministic system $W = (I_W, F_W)$ ("watch").
  - $State := \mathbb{N}_{24} \times \mathbb{N}_{60}$.
    - $\mathbb{N}_n := \{i \in \mathbb{N} : i < n\}$.
  - $I_W(h, m) :\Leftrightarrow h = 0 \wedge m = 0$.
    - $I_W := \{\langle h, m \rangle : h = 0 \wedge m = 0\} = \{\langle 0, 0 \rangle\}$.
  - $F_W(h, m) :=$
    if $m < 59$ **then** $\langle h, m+1 \rangle$
    else if $h < 23$ **then** $\langle h+1, 0 \rangle$
    else $\langle 0, 0 \rangle$.
- A nondeterministic system $C = (I_C, R_C)$ (modulo 3 "counter").
  - $State := \mathbb{N}_3$.
  - $I_C(i) :\Leftrightarrow i = 0$.
  - $R_C(i, i') :\Leftrightarrow inc(i, i') \vee dec(i, i')$.
    - $inc(i, i') :\Leftrightarrow$ **if** $i < 2$ **then** $i' = i + 1$ **else** $i' = 0$.
    - $dec(i, i') :\Leftrightarrow$ **if** $i > 0$ **then** $i' = i - 1$ **else** $i' = 2$.

Composing Systems

Compose n components S_i to a concurrent system S.

- State space $State := State_0 \times \ldots \times State_{n-1}$.
  - $State_i$ is the state space of component i.
  - State space is Cartesian product of component state spaces.
  - Size of state space is product of the sizes of the component spaces.
- Example: three counters with state spaces $\mathbb{N}_2$ and $\mathbb{N}_3$ and $\mathbb{N}_4$.



B.Berard et al: "Systems and Software Verification", 2001.

Initial States of Composed System

What are the initial states I of the composed system?

- Set $I := I_0 \times \ldots \times I_{n-1}$.
  - I_i is the set of initial states of component i.
  - Set of initial states is Cartesian product of the sets of initial states of the individual components.
- Predicate $I(s_0, \ldots, s_{n-1}) :\Leftrightarrow I_0(s_0) \wedge \ldots \wedge I_{n-1}(s_{n-1})$.
  - I_i is the initial state condition of component i.
  - Initial state condition is conjunction of the initial state conditions of the components on the corresponding projection of the state.

Size of initial state set is the product of the sizes of the initial state sets of the individual components.

Transitions of Composed System

Which transitions can the composed system perform?

- Synchronized composition.
  - At each step, every component must perform a transition.
    - R_i is the transition relation of component i.
      $R(\langle s_0, \ldots, s_{n-1} \rangle, \langle s'_0, \ldots, s'_{n-1} \rangle) :\Leftrightarrow$
      $R_0(s_0, s'_0) \wedge \ldots \wedge R_{n-1}(s_{n-1}, s'_{n-1})$.
- Asynchronous composition.
  - At each moment, every component may perform a transition.
    - At least one component performs a transition.
    - Multiple simultaneous transitions are possible
    - With n components, $2^n - 1$ possibilities of (combined) transitions.
      $R(\langle s_0, \ldots, s_{n-1} \rangle, \langle s'_0, \ldots, s'_{n-1} \rangle) :\Leftrightarrow$
      $(R_0(s_0, s'_0) \wedge \ldots \wedge s_{n-1} = s'_{n-1}) \vee$
      $\ldots$
      $(s_0 = s'_0 \wedge \ldots \wedge R_{n-1}(s_{n-1}, s'_{n-1})) \vee$
      $\ldots$
      $(R_0(s_0, s'_0) \wedge \ldots \wedge R_{n-1}(s_{n-1}, s'_{n-1}))$.

Example

System of three counters with state space $\mathbb{N}_2$ each.

- Synchronous composition:

$$[0,0,0] \leftrightarrows [1,1,1]$$

- Asynchronous composition:

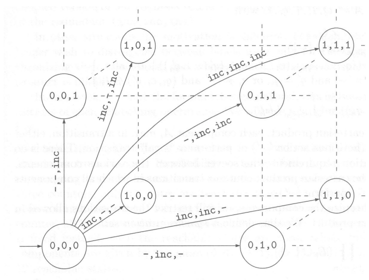


Fig. 1.10. A few transitions of the product of the three counters

B.Berard et al: "Systems and Software Verification", 2001.

Interleaving Execution

Simplified view of asynchronous execution.

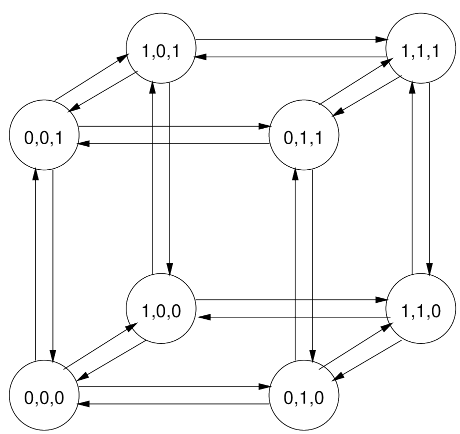
- At each moment, only one component performs a transition.
  - Do not allow simultaneous transition $t_i|t_j$ of two components i and j.
  - Transition sequences $t_i; t_j$ and $t_j; t_i$ are possible.
    - All possible interleavings of component transitions are considered.
    - Nondeterminism is used to simulate concurrency.
    - Essentially no change of system properties.
  - With n components, only n possibilities of a transition.

$$\begin{aligned}
&R(\langle s_0, s_1, \ldots, s_{n-1}\rangle, \langle s'_0, s'_1, \ldots, s'_{n-1}\rangle) :\Leftrightarrow\\
&\quad (R_0(s_0, s'_0) \wedge s_1 = s'_1 \wedge \ldots \wedge s_{n-1} = s'_{n-1}) \vee\\
&\quad (s_0 = s'_0 \wedge R_1(s_1, s'_1) \wedge \ldots \wedge s_{n-1} = s'_{n-1}) \vee\\
&\quad \ldots\\
&\quad (s_0 = s'_0 \wedge s_1 = s'_1 \wedge \ldots \wedge R_{n-1}(s_{n-1}, s'_{n-1})).
\end{aligned}$$

Interleaving model (respectively a variant of it) suffices in practice.

Example

System of three counters with state space $\mathbb{N}_2$ each.



Digital Circuits

Synchronous composition of hardware components.

- A modulo 8 counter $C = \langle I_C, R_C\rangle$.

$State := \mathbb{N}_2 \times \mathbb{N}_2 \times \mathbb{N}_2.$

$I_C(v_0, v_1, v_2) :\Leftrightarrow v_0 = v_1 = v_2 = 0.$

$$\begin{aligned}
&R_C(\langle v_0, v_1, v_2\rangle, \langle v'_0, v'_1, v'_2\rangle) :\Leftrightarrow\\
&\quad R_0(v_0, v'_0) \wedge\\
&\quad R_1(v_0, v_1, v'_1) \wedge\\
&\quad R_2(v_0, v_1, v_2, v'_2).
\end{aligned}$$

$R_0(v_0, v'_0) :\Leftrightarrow v'_0 = \neg v_0.$
$R_1(v_0, v_1, v'_1) :\Leftrightarrow v'_1 = v_0 \oplus v_1.$
$R_2(v_0, v_1, v_2, v'_2) :\Leftrightarrow v'_2 = (v_0 \wedge v_1) \oplus v_2.$

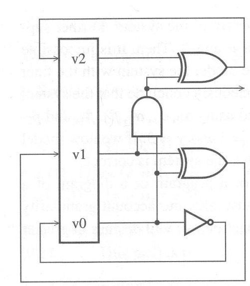


Figure 2.1
Synchronous modulo 8 counter.

Edmund Clarke et al: "Model Checking", 1999.

Concurrent Software

Asynchronous composition of software components with shared variables.

P :: l_0 : **while** true **do**
  NC_0 : **wait** $turn = 0$
  CR_0 : $turn := 1$
end

||

Q :: l_1 : **while** true **do**
  NC_1 : **wait** $turn = 1$
  CR_1 : $turn := 0$
end

- A mutual exclusion program $M = \langle I_M, R_M \rangle$.

  $State := PC \times PC \times \mathbb{N}_2$. // shared variable
  $I_M(p, q, turn) :\Leftrightarrow p = l_0 \wedge q = l_1$.
  $R_M(\langle p, q, turn \rangle, \langle p', q', turn' \rangle) :\Leftrightarrow$
  $(P(\langle p, turn \rangle, \langle p', turn' \rangle) \wedge q' = q) \vee (Q(\langle q, turn \rangle, \langle q', turn' \rangle) \wedge p' = p)$.
  $P(\langle p, turn \rangle, \langle p', turn' \rangle) :\Leftrightarrow$
  $(p = l_0 \wedge p' = NC_0 \wedge turn' = turn) \vee$
  $(p = NC_0 \wedge p' = CR_0 \wedge turn = 0 \wedge turn' = turn) \vee$
  $(p = CR_0 \wedge p' = l_0 \wedge turn' = 1)$.
  $Q(\langle q, turn \rangle, \langle q', turn' \rangle) :\Leftrightarrow$
  $(q = l_1 \wedge q' = NC_1 \wedge turn' = turn) \vee$
  $(q = NC_1 \wedge q' = CR_1 \wedge turn = 1 \wedge turn' = turn) \vee$
  $(q = CR_1 \wedge q' = l_1 \wedge turn' = 0)$.

Concurrent Software

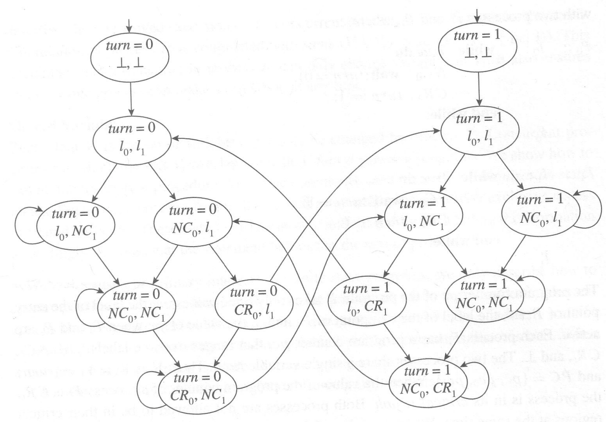


Figure 2.2
Reachable states of Kripke structure for mutual exclusion example.

Edmund Clarke et al: "Model Checking", 1999.

Model guarantees mutual exclusion.

Modeling Commands

Transition relations are typically described in a particular form.

- $R(s, s') :\Leftrightarrow P(s) \wedge s' = F(s)$.
  - Guard condition P on state in which transition can be performed.
    - If $P(s)$ holds, then there exists some s' such that $R(s, s')$.
  - Transition function F that determines the successor of s.
    - F is defined for all states for which $P(s)$ holds:
      $F : \{s \in State : P(s)\} \to State$.
- Examples:
  - Assignment: $l : x := e;\ m : \ldots$.
    - $R(\langle pc, x, y \rangle, \langle pc', x', y' \rangle) :\Leftrightarrow pc = l \wedge (x' = e \wedge y' = y \wedge pc' = m)$.
  - Wait statement: l : **wait** $P(x, y);\ m : \ldots$.
    - $R(\langle pc, x, y \rangle, \langle pc', x', y' \rangle) :\Leftrightarrow$
      $pc = l \wedge P(x, y) \wedge (x' = x \wedge y' = y \wedge pc' = m)$.
  - Guarded assignment: $l : P(x, y) \to x := e;\ m : \ldots$.
    - $R(\langle pc, x, y \rangle, \langle pc', x', y' \rangle) :\Leftrightarrow$
      $pc = l \wedge P(x, y) \wedge (x' = e \wedge y' = y \wedge pc' = m)$.

Most programming language commands can be translated into this form.

Modelling Message Passing Systems

How to model an asynchronous system without shared variables where the components communicate/synchronize by exchanging messages?

- Given a label set $Label = Int \cup Ext \cup \overline{Ext}$.
  - Disjoint sets Int and Ext of internal and external labels.
    - "Anonymous" label $\_ \in Int$.
  - Complementary label set $\overline{L} := \{\bar{l} : l \in L\}$.
- A labeled system is a pair $\langle I, R \rangle$.
  - Initial state condition $I \subseteq State$.
  - Labeled transition relation $R \subseteq Label \times State \times State$.
- A run of a labeled system $\langle I, R \rangle$ is a (finite or infinite) sequence $s_0 \xrightarrow{l_0} s_1 \xrightarrow{l_1} \ldots$ of states such that
  - $s_0 \in I$.
  - $R(l_i, s_i, s_{i+1})$ (for all sequence indices i).
  - If s ends in a state s_n, there is no label l and state t s.t. $R(l, s_n, t)$.

Synchronization by Message Passing

Compose a set of n labeled systems $\langle I_i, R_i \rangle$ to a system $\langle I, R \rangle$.

- State space $State := State_0 \times \ldots \times State_{n-1}$.
- Initial states $I := I_0 \times \ldots \times I_{n-1}$.
  - $I(s_0, \ldots, s_{n-1}) :\Leftrightarrow I_0(s_0) \wedge \ldots \wedge I_{n-1}(s_{n-1})$.
- Transition relation

$$
\begin{array}{l}
R(l, \langle s_i \rangle_{i \in \mathbb{N}_n}, \langle s'_i \rangle_{i \in \mathbb{N}_n}) \Leftrightarrow \\
\quad (l \in Int \wedge \exists i \in \mathbb{N}_n : \\
\qquad R_i(l, s_i, s'_i) \wedge \forall k \in \mathbb{N}_n \backslash \{i\} : s_k = s'_k) \vee \\
\quad (l = \_ \wedge \exists l \in Ext, i \in \mathbb{N}_n, j \in \mathbb{N}_n : \\
\qquad R_i(l, s_i, s'_i) \wedge R_j(\bar{l}, s_j, s'_j) \wedge \forall k \in \mathbb{N}_n \backslash \{i, j\} : s_k = s'_k).
\end{array}
$$

Either a component performs an internal transition or two components simultaneously perform an external transition with complementary labels.

Communication by Message Passing

$$
\begin{array}{lll}
0 :: \textbf{loop} & & 1 :: \textbf{loop} \\
\quad a_0 : \textbf{send}(i) & & \quad b_0 : j := \textbf{receive}() \\
\quad a_1 : i := \textbf{receive}() & \| & \quad b_1 : j := j + 1 \\
\quad a_2 : i := i + 1 & & \quad b_2 : \textbf{send}(j) \\
\textbf{end} & & \textbf{end}
\end{array}
$$

- Two labeled systems $\langle I_0, R_0 \rangle$ and $\langle I_1, R_1 \rangle$.

$$
\begin{array}{l}
State_0 = State_1 = PC \times \mathbb{N},\ Internal := \{A, B\}, External := \{M, N\}. \\
I_0(p, i) :\Leftrightarrow p = a_0 \wedge i \in \mathbb{N};\ I_1(q, j) :\Leftrightarrow q = b_0. \\
R_0(l, \langle p, i \rangle, \langle p', i' \rangle) :\Leftrightarrow \\
\quad (l = \overline{M} \wedge p = a_0 \wedge p' = a_1 \wedge i' = i)\ \vee \\
\quad (l = N \wedge p = a_1 \wedge p' = a_2 \wedge i' = j)\ \vee\ \text{// illegal!} \\
\quad (l = A \wedge p = a_2 \wedge p' = a_0 \wedge i' = i + 1). \\
R_1(l, \langle q, j \rangle, \langle q', j' \rangle) :\Leftrightarrow \\
\quad (l = M \wedge q = b_0 \wedge q' = b_1 \wedge j' = i)\ \vee\ \text{// illegal!} \\
\quad (l = B \wedge q = b_1 \wedge q' = b_2 \wedge j' = j + 1)\ \vee \\
\quad (l = \overline{N} \wedge q = b_2 \wedge q' = b_0 \wedge j' = j).
\end{array}
$$

Example (Continued)

Composition of $\langle I_0, R_0 \rangle$ and $\langle I_1, R_1 \rangle$ to $\langle I, R \rangle$.

$State = (PC \times \mathbb{N}) \times (PC \times \mathbb{N})$.

$I(p, i, q, j) :\Leftrightarrow p = a_0 \wedge i \in \mathbb{N} \wedge q = b_0$.

$$
\begin{array}{l}
R(l, \langle p, i, q, j \rangle, \langle p', i', q', j' \rangle) :\Leftrightarrow \\
\quad (l = A \wedge (p = a_2 \wedge p' = a_0 \wedge i' = i + 1) \wedge (q' = q \wedge j' = j)) \vee \\
\quad (l = B \wedge (p' = p \wedge i' = i) \wedge (q = b_1 \wedge q' = b_2 \wedge j' = j + 1))\ \vee \\
\quad (l = \_\ \wedge (p = a_0 \wedge p' = a_1 \wedge i' = i) \wedge (q = b_0 \wedge q' = b_1 \wedge j' = i)) \vee \\
\quad (l = \_\ \wedge (p = a_1 \wedge p' = a_2 \wedge i' = j) \wedge (q = b_2 \wedge q' = b_0 \wedge j' = j)).
\end{array}
$$

Problem: state relation of each component refers to local variable of other component (variables are shared).

Example (Revised)

$$
\begin{array}{lll}
0 :: \textbf{loop} & & 1 :: \textbf{loop} \\
\quad a_0 : \textbf{send}(i) & & \quad b_0 : j := \textbf{receive}() \\
\quad a_1 : i := \textbf{receive}() & \| & \quad b_1 : j := j + 1 \\
\quad a_2 : i := i + 1 & & \quad b_2 : \textbf{send}(j) \\
\textbf{end} & & \textbf{end}
\end{array}
$$

- Two labeled systems $\langle I_0, R_0 \rangle$ and $\langle I_1, R_1 \rangle$.

$$
\begin{array}{l}
\ldots \\
External := \{M_k : k \in \mathbb{N}\} \cup \{N_k : k \in \mathbb{N}\}. \\
R_0(l, \langle p, i \rangle, \langle p', i' \rangle) :\Leftrightarrow \\
\quad (l = \overline{M_i} \wedge p = a_0 \wedge p' = a_1 \wedge i' = i)\ \vee \\
\quad (\exists k \in \mathbb{N} : l = N_k \wedge p = a_1 \wedge p' = a_2 \wedge i' = k)\ \vee \\
\quad (l = A \wedge p = a_2 \wedge p' = a_0 \wedge i' = i + 1). \\
R_1(l, \langle q, j \rangle, \langle q', j' \rangle) :\Leftrightarrow \\
\quad (\exists k \in \mathbb{N} : l = M_k \wedge q = b_0 \wedge q' = b_1 \wedge j' = k)\ \vee \\
\quad (l = B \wedge q = b_1 \wedge q' = b_2 \wedge j' = j + 1)\ \vee \\
\quad (l = \overline{N_j} \wedge q = b_2 \wedge q' = b_0 \wedge j' = j).
\end{array}
$$

Encode message value in label.

Example (Continued)

Composition of $\langle I_0, R_0\rangle$ and $\langle I_1, R_1\rangle$ to $\langle I, R\rangle$.

$$State = (PC \times \mathbb{N}) \times (PC \times \mathbb{N}).$$

$$I(p,i,q,j) :\Leftrightarrow p = a_0 \wedge i \in \mathbb{N} \wedge q = b_0.$$

$$\begin{aligned}
&R(l, \langle p,i,q,j\rangle, \langle p',i',q',j'\rangle) :\Leftrightarrow \\
&\quad (l = A \wedge (p = a_2 \wedge p' = a_0 \wedge i' = i+1) \wedge (q' = q \wedge j' = j)) \vee \\
&\quad (l = B \wedge (p' = p \wedge i' = i) \wedge (q = b_1 \wedge q' = b_2 \wedge j' = j+1)) \vee \\
&\quad (l = _ \wedge \exists k \in \mathbb{N} : k = i \wedge \\
&\qquad (p = a_0 \wedge p' = a_1 \wedge i' = i) \wedge (q = b_0 \wedge q' = b_1 \wedge j' = k)) \vee \\
&\quad (l = _ \wedge \exists k \in \mathbb{N} : k = j \wedge \\
&\qquad (p = a_1 \wedge p' = a_2 \wedge i' = k) \wedge (q = b_2 \wedge q' = b_0 \wedge j' = j)).
\end{aligned}$$

Logically equivalent to previous definition of transition relation.

The Client/Server System

Asynchronous composition of three components $Client_1$, $Client_2$, $Server$.

- $Client_i$: $State := PC \times \mathbb{N}_2 \times \mathbb{N}_2$.
  - Three variables $pc, request, answer$.
  - pc represents the program counter.
  - $request$ is the buffer for outgoing requests.
    - Filled by client, when a request is to be sent to server.
  - $answer$ is the buffer for incoming answers.
    - Checked by client, when it waits for an answer from the server.
- $Server$: $State := (\mathbb{N}_3)^3 \times (\{1,2\} \to \mathbb{N}_2)^2$.
  - Variables $given, waiting, sender, rbuffer, sbuffer$.
  - No program counter.
    - We use the value of $sender$ to check whether server waits for a request ($sender = 0$) or answers a request ($sender \neq 0$).
  - Variables $given, waiting, sender$ as in program.
  - $rbuffer(i)$ is the buffer for incoming requests from client i.
  - $sbuffer(i)$ is the buffer for outgoing answers to client i.

External Transitions

- $Ext := \{REQ_1, REQ_2, ANS_1, ANS_2\}$.
  - Transition labeled REQ_i transmits a request from client i to server.
    - Enabled when $request \neq 0$ in client i.
    - Effect in client i: $request' = 0$.
    - Effect in server: $rbuffer'(i) = 1$.
  - Transition labeled ANS_i transmits an answer from server to client i
    - Enabled when $sbuffer(i) \neq 0$.
    - Effect in server: $sbuffer'(i) = 0$.
    - Effect in client i: $answer' = 1$.

The external transitions correspond to system-level actions of the communication subsystem (rather than to the user-level actions of the client/server program).

The Client

Client system $C_i = \langle IC_i, RC_i \rangle$.

$State := PC \times \mathbb{N}_2 \times \mathbb{N}_2$.
$Int := \{R_i, S_i, C_i\}$.

$IC_i(pc, request, answer) :\Leftrightarrow$
$\quad pc = R \wedge request = 0 \wedge answer = 0.$
$RC_i(I, \langle pc, request, answer \rangle,$
$\quad \langle pc', request', answer' \rangle) :\Leftrightarrow$
$\quad (I = R_i \wedge pc = R \wedge request = 0 \wedge$
$\quad\quad pc' = S \wedge request' = 1 \wedge answer' = answer) \vee$
$\quad (I = S_i \wedge pc = S \wedge answer \neq 0 \wedge$
$\quad\quad pc' = C \wedge request' = request \wedge answer' = 0) \vee$
$\quad (I = C_i \wedge pc = C \wedge request = 0 \wedge$
$\quad\quad pc' = R \wedge request' = 1 \wedge answer' = answer) \vee$

$\quad (I = \overline{REQ_i} \wedge request \neq 0 \wedge$
$\quad\quad pc' = pc \wedge request' = 0 \wedge answer' = answer) \vee$
$\quad (I = ANS_i \wedge$
$\quad\quad pc' = pc \wedge request' = request \wedge answer' = 1).$

```
Client(ident):
  param ident
begin
  loop
    ...
 R: sendRequest()
 S: receiveAnswer()
 C: // critical region
    ...
    sendRequest()
  endloop
end Client
```


The Server

Server system $S = \langle IS, RS \rangle$.

$State := (\mathbb{N}_3)^3 \times (\{1,2\} \to \mathbb{N}_2)^2$.
$Int := \{D1, D2, F, A1, A2, W\}$.

$IS(given, waiting, sender, rbuffer, sbuffer) :\Leftrightarrow$
$\quad given = waiting = sender = 0 \wedge$
$\quad rbuffer(1) = rbuffer(2) = sbuffer(1) = sbuffer(2) = 0.$

$RS(I, \langle given, waiting, sender, rbuffer, sbuffer \rangle,$
$\quad\quad \langle given', waiting', sender', rbuffer', sbuffer' \rangle) :\Leftrightarrow$
$\quad \exists i \in \{1,2\}:$
$\quad\quad (I = D_i \wedge sender = 0 \wedge rbuffer(i) \neq 0 \wedge$
$\quad\quad sender' = i \wedge rbuffer'(i) = 0 \wedge$
$\quad\quad U(given, waiting, sbuffer) \wedge$
$\quad\quad \forall j \in \{1,2\} \backslash \{i\} : U_j(rbuffer)) \vee$
$\quad \ldots$

$U(x_1, \ldots, x_n) :\Leftrightarrow x_1' = x_1 \wedge \ldots \wedge x_n' = x_n.$
$U_j(x_1, \ldots, x_n) :\Leftrightarrow x_1'(j) = x_1(j) \wedge \ldots \wedge x_n'(j) = x_n(j).$

```
Server:
  local given, waiting, sender
begin
  given := 0; waiting := 0
  loop
D:  sender := receiveRequest()
    if sender = given then
      if waiting = 0 then
F:      given := 0
      else
A1:     given := waiting;
        waiting := 0
        sendAnswer(given)
      endif
    elsif given = 0 then
A2:   given := sender
      sendAnswer(given)
    else
W:    waiting := sender
    endif
  endloop
end Server
```


The Server (Contd)

$\quad \ldots$
$\quad (I = F \wedge sender \neq 0 \wedge sender = given \wedge waiting = 0 \wedge$
$\quad\quad given' = 0 \wedge sender' = 0 \wedge$
$\quad\quad U(waiting, rbuffer, sbuffer)) \vee$

$\quad (I = A1 \wedge sender \neq 0 \wedge sbuffer(waiting) = 0 \wedge$
$\quad\quad sender = given \wedge waiting \neq 0 \wedge$
$\quad\quad given' = waiting \wedge waiting' = 0 \wedge$
$\quad\quad sbuffer'(waiting) = 1 \wedge sender' = 0 \wedge$
$\quad\quad U(rbuffer) \wedge$
$\quad\quad \forall j \in \{1,2\} \backslash \{waiting\} : U_j(sbuffer)) \vee$

$\quad (I = A2 \wedge sender \neq 0 \wedge sbuffer(sender) = 0 \wedge$
$\quad\quad sender \neq given \wedge given = 0 \wedge$
$\quad\quad given' = sender \wedge$
$\quad\quad sbuffer'(sender) = 1 \wedge sender' = 0 \wedge$
$\quad\quad U(waiting, rbuffer) \wedge$
$\quad\quad \forall j \in \{1,2\} \backslash \{sender\} : U_j(sbuffer)) \vee$
$\quad \ldots$

```
Server:
  local given, waiting, sender
begin
  given := 0; waiting := 0
  loop
D:  sender := receiveRequest()
    if sender = given then
      if waiting = 0 then
F:      given := 0
      else
A1:     given := waiting;
        waiting := 0
        sendAnswer(given)
      endif
    elsif given = 0 then
A2:   given := sender
      sendAnswer(given)
    else
W:    waiting := sender
    endif
  endloop
end Server
```


The Server (Contd'2)

$\quad \ldots$
$\quad (I = W \wedge sender \neq 0 \wedge sender \neq given \wedge given \neq 0 \wedge$
$\quad\quad waiting' := sender \wedge sender' = 0 \wedge$
$\quad\quad U(given, rbuffer, sbuffer)) \vee$

$\quad \exists i \in \{1,2\}:$

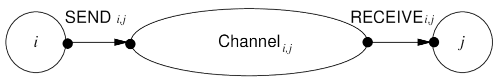
$\quad\quad (I = REQ_i \wedge rbuffer'(i) = 1 \wedge$
$\quad\quad\quad U(given, waiting, sender, sbuffer) \wedge$
$\quad\quad\quad \forall j \in \{1,2\} \backslash \{i\} : U_j(rbuffer)) \vee$

$\quad\quad (I = \overline{ANS_i} \wedge sbuffer(i) \neq 0 \wedge$
$\quad\quad\quad sbuffer'(i) = 0 \wedge$
$\quad\quad\quad U(given, waiting, sender, rbuffer) \wedge$
$\quad\quad\quad \forall j \in \{1,2\} \backslash \{i\} : U_j(sbuffer)).$

```
Server:
  local given, waiting, sender
begin
  given := 0; waiting := 0
  loop
D:  sender := receiveRequest()
    if sender = given then
      if waiting = 0 then
F:      given := 0
      else
A1:     given := waiting;
        waiting := 0
        sendAnswer(given)
      endif
    elsif given = 0 then
A2:   given := sender
      sendAnswer(given)
    else
W:    waiting := sender
    endif
  endloop
end Server
```

Communication Channels

We also model the communication medium between components.



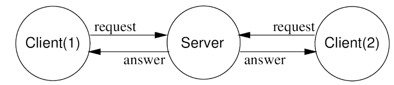
- Bounded channel $Channel_{i,j} = (ICH, RCH_{i,j})$.
  - Transfers message from component with address i to component j.
    - May hold at most N messages at a time (for some N).
  - $State := Value^*$.
    - Sequence of values of type $Value$.
  - $Ext := \{SEND_{i,j}(m) : m \in Value\} \cup \{RECEIVE_{i,j}(m) : m \in Value\}$.
    - By $SEND_{i,j}(m)$, channel receives from sender i a message m destined for receiver j; by $RECEIVE_{i,j}(m)$, channel forwards that message.

  $ICH(queue) :\Leftrightarrow queue = \langle\rangle$.

  $RCH_{i,j}(l, queue, queue') :\Leftrightarrow$
  $\exists m \in Value :$
  $(l = SEND_{i,j}(m) \wedge |queue| < N \wedge queue' = queue \circ \langle m \rangle) \vee$
  $(l = \overline{RECEIVE}_{i,j}(m) \wedge |queue| > 0 \wedge queue = \langle m \rangle \circ queue')$.

Client/Server Example with Channels

- Server receives address 0.
  - Label REQ_i is renamed to $RECEIVE_{i,0}(R)$.
  - Label $\overline{ANS_i}$ is renamed to $\overline{SEND_{0,i}(A)}$.
- Client i receives address i ($i \in \{1, 2\}$).
  - Label $\overline{REQ_i}$ is renamed to $\overline{SEND_{i,0}(R)}$.
  - Label ANS_i is renamed to $RECEIVE_{0,i}(A)$.
- System is composed of seven components:
  - $Server$, $Client_1$, $Client_2$.
  - $Channel_{0,1}$, $Channel_{1,0}$.
  - $Channel_{0,2}$, $Channel_{2,0}$.



Also channels are active system components.

Summary

- A system is described by
  - its (finite or infinite) state space,
  - the initial state condition (set of input states),
  - the transition relation on states.
- State space of composed system is product of component spaces.
  - Variable shared among components occurs only once in product.
- System composition can be
  - synchronous: conjunction of individual transition relations.
    - Suitable for digital hardware.
  - asynchronous: disjunction of relations.
    - Interleaving model: each relation conjoins the transition relation of one component with the identity relations of all other components.
    - Suitable for concurrent software.
- Message passing systems may be modeled by using labels:
  - Synchronize transitions of sender and receiver.
  - Carry values to be transmitted from sender to receiver.